

## TWO-WAY MULTIHEAD AUTOMATA OVER A ONE-LETTER ALPHABET (\*)

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**Abstract.** — *Let  $H(k)$  and  $NH(k)$  denote the classes of languages over a one-letter alphabet acceptable by deterministic (respectively nondeterministic) two-way  $k$ -head finite automata. It will be shown that  $H(k) \not\subseteq H(k+1)$  and  $NH(k) \not\subseteq NH(k+1)$  holds for all  $k \geq 1$ . Hierarchy results are also proved for the classes of languages over a one-letter alphabet defined by  $k$ -counter automata with linear bounded counters and by  $k$ -register machines with linear bounded registers, respectively.*

**Résumé.** — *On désigne par  $H(k)$  et  $NH(k)$  les familles des langages sur un alphabet à une seule lettre qui sont reconnaissables par des automates finis bilatères à  $k$  têtes déterministes (resp. non déterministes). On montre que  $H(k) \not\subseteq H(k+1)$  et que  $NH(k) \not\subseteq NH(k+1)$  pour tout  $k \geq 1$ . On donne également une hiérarchie dans les familles de langages définies par des automates à  $k$ -c ompteurs linéairement bornés et par des machines à  $k$ -registres linéairement bornés respectivement.*

### 1. INTRODUCTION

In this paper we show that for languages over a one-letter alphabet two-way automata with  $k+1$  heads are more powerful than two-way automata with  $k$  heads.

This result is related to the results of [3 and 4] concerning the refinement of complexity classes. It is wellknown that  $SPACE(\log n)$ , the class of languages acceptable within space bound  $\log n$ , is identical with the class of languages acceptable by two-way multihead automata. Each two-way multihead automaton can be viewed as a  $\log n$ -space bounded machine with restricted storage abilities.

It was shown before that over a one-letter alphabet two-way  $(k+4)$ -head automata are more powerful than  $k$ -head automata ([4, 5]) and that over a two-letter alphabet  $k+1$  heads are more powerful than  $k$  ([2]; in [1] it was shown that

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$k + 2$  heads are more powerful than  $k$  heads). It is also known that  $k + 1$  heads are better than  $k$  for one-way automata [6].

The method used here to show that  $k + 1$  heads are more powerful than  $k$  for two-way automata, even for a one-letter alphabet, is similar to the method used in [1 to 5]. We define transformations which map multihead languages onto languages defined by fewer heads. These transformations allow us to use the assumption " $k + 1$  heads have the same power as  $k$  heads" repeatedly to arrive at a contradiction.

We also prove hierarchy results for the classes of languages over a one-letter alphabet defined by  $k$ -counter automata and by  $k$ -register machines whose counters (or registers) are linearly bounded by the length of the input.

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## 2. DEFINITIONS AND RESULTS

A *two-way  $k$ -head automaton* consists of a finite control and an input tape where  $k$  heads may move independently in both directions. The input is placed between two endmarkers ( $-$  and  $+$ ). The automaton starts in a distinguished starting state with its  $k$  heads on the left endmarker. It accepts the input string if it stops in an accepting state. The automaton is called deterministic if its next move function is deterministic, otherwise it is called nondeterministic. Let  $H_{\Sigma}(k)$  [ $NH_{\Sigma}(k)$ ], be the class of languages over the alphabet  $\Sigma$  acceptable by deterministic [nondeterministic] two-way  $k$ -head automata.

A *two-way  $k$ -counter automaton* consists of a finite control, an input tape where one head is moving in both directions and  $k$  counters. With  $C_{\Sigma}(k)$  [ $NC_{\Sigma}(k)$ ] we denote the class of all languages over  $\Sigma$  acceptable by deterministic [nondeterministic]  $k$ -counter automata whose counters are always linearly bounded by the length of the input.

A  *$k$ -register machine* consists of a finite control and  $k$  registers. (In fact a register is just the same as a counter, namely a storage unit which can store one natural number and on which the operations  $+1$ ,  $-1$  and the predicate  $\stackrel{?}{=} 0$  can be carried out.)

The machine starts with the input number in register 1 and the other registers storing zero. Note that such a machine can destroy its input number. It accepts an input number by reaching an accepting state. A register machine accepts a subset of  $\mathbb{N} \cup \{0\}$ . Throughout this paper we denote by  $\mathbb{N}$  the set of natural

numbers. Let  $R(k)$  [ $RN(k)$ ] be the class of all languages  $L \subset \{0\}^*$  such that  $\{n \mid 0^n \in L\}$  is accepted by a deterministic [nondeterministic]  $k$ -register machine whose registers are always linearly bounded by the length of the input.

Let us furthermore denote by  $SPACE_{\Sigma}(L(n))$  [ $NSPACE_{\Sigma}(L(n))$ ] the class of languages over the alphabet  $\Sigma$  which are acceptable by deterministic [nondeterministic] Turing machines within space bound  $L(n)$ .

It is straightforward to see that for every  $k \geq 1$ ,

$$R(k) \subset C_{\{0\}}(k-1) \subset H_{\{0\}}(k) \subset C_{\{0\}}(k) \subset R(k+2)$$

and that

$$\bigcup_{k \in \mathbb{N}} R(k) = \bigcup_{k \in \mathbb{N}} C_{\{0\}}(k) = \bigcup_{k \in \mathbb{N}} H_{\{0\}}(k) = SPACE_{\{0\}}(\log n).$$

The corresponding result holds in the nondeterministic case.

In the following we only consider languages  $L \subset \{0^{2^n}; n \in \mathbb{N}\}$ . Let  $\tilde{H}(k)$  be the class of all languages  $L$  such that  $L \in H_{\{0\}}(k)$  and  $L \subset \{0^{2^n}; n \in \mathbb{N}\}$ . In the same way we will interpret the superscript  $\sim$  for the other complexity classes occurring in this paper.

We use the mapping  $T_k : \{2^n \mid n \in \mathbb{N}\} \rightarrow \{2^n \mid n \in \mathbb{N}\}$  defined by  $T_k(2^n) = 2^{k \cdot n}$  and whenever it is appropriate we will identify  $\{0\}^*$  and  $\mathbb{N}$ .

First we will prove the following lemmas:

LEMMA 1:  $\tilde{H}(k) \not\subseteq \widetilde{SPACE}(\log n)$ ,  $\widetilde{NH}(k) \not\subseteq \widetilde{NSPACE}(\log n)$  for all  $k \in \mathbb{N}$ .

LEMMA 2: For all  $L \in \widetilde{SPACE}(\log n)$  [ $\widetilde{NSPACE}(\log n)$ ] there exists a number  $k \in \mathbb{N}$  such that  $T_k(L) \in \tilde{R}(3)$  [ $\tilde{NR}(3)$ ].

LEMMA 3: For all  $L \in \widetilde{SPACE}(\log n)$  and for  $k, j \geq 1$ :

$$T_k(L) \in \tilde{H}(j) [\widetilde{NH}(j)] \Rightarrow L \in \tilde{H}(k \cdot j) [\widetilde{NH}(k \cdot j)].$$

LEMMA 4: For all  $L \in \widetilde{SPACE}(\log n)$  and for  $k > j \geq 2$ :

$$T_{k+1}(L) \in \tilde{H}(j) [\widetilde{NH}(j)] \Rightarrow T_k(L) \in \tilde{H}(j+1) [\widetilde{NH}(j+1)].$$

From these lemmas our first theorem follows immediately.

THEOREM 1: For all  $j \in \mathbb{N}$ :

$$H_{\{0\}}(j) \not\subseteq H_{\{0\}}(j+1) \quad \text{and} \quad NH_{\{0\}}(j) \not\subseteq NH_{\{0\}}(j+1).$$

*Proof:* We give the proof only for the deterministic case. The proof for the nondeterministic case is exactly the same.

The result is true for  $j=1$  since  $\{O^{2^*}; n \in \mathbb{N}\} \in H_{\{0\}}(2) - H_{\{0\}}(1)$ .

Now suppose there exists some  $j \geq 2$  such that  $H_{\{0\}}(j) = H_{\{0\}}(j+1)$ . This implies  $\tilde{H}(j) = \tilde{H}(j+1)$  and therefore the following is true

$$\begin{aligned} L \in \widetilde{\text{SPACE}}(\log n) & \\ \Rightarrow \exists k, T_k(L) \in \tilde{R}(3) \subset \tilde{H}(3) & \text{ lemma 2} \\ \Rightarrow T_k(L) \in \tilde{H}(j+1) = \tilde{H}(j) & \\ \Rightarrow T_{k-1}(L) \in \tilde{H}(j+1) = \tilde{H}(j) & \text{ lemma 4} \Rightarrow \dots \\ \Rightarrow T_{j+1}(L) \in \tilde{H}(j+1) = \tilde{H}(j) & \text{ lemma 4} \\ \Rightarrow L \in \tilde{H}((j+1) \cdot j) & \text{ lemma 3.} \end{aligned}$$

Therefore  $H_{\{0\}}(j) = H_{\{0\}}(j+1)$  implies  $\widetilde{\text{NSPACE}}(\log n) \subset \tilde{H}(j \cdot (j+1))$  which is a contradiction to lemma 1.  $\square$

We will prove lemma 1, ..., 4 in section 3. The proofs of lemma 1, 2 and 3 and (as it could be seen already) the proof of theorem 1 are not really difficult. The central point in this paper is the proof of lemma 4.

In section 4 we will formulate and prove the hierarchy results for the classes defined by counter automata and register machines.

### 3. PROOFS OF THE BASIC LEMMAS

In all the proofs there is no difference at all between the nondeterministic and the deterministic case. Therefore we will always consider only the deterministic case.

#### Proof of lemma 1

In this proof we use two results from [4] and we denote by  $\text{Space}_x(L(n), m)$  the class of all languages over  $\Sigma$  which are accepted by Turing machines which have one input tape, one worktape, one read-only input head, one work tape head,  $m$  work tape symbols and which operate on its worktape with the tape bound  $L(n)$ .

In [4] it was proved that

$$H_x(k) \subset \text{Space}_x(\log_2 n, 2^k)$$

and that

$$\text{Space}_{\{0\}}(n) \neq \text{Space}_{\{0\}}(n, m)$$

for every  $m \in \mathbb{N}$ .

Now let  $\text{bi} : \mathbb{N} \rightarrow \{0, 1\}^*$  be the bijective mapping defined by:  $\text{bi}(n) = \varphi \Leftrightarrow 1 \varphi$  is the binary notation of  $n$ . Let  $\text{un} : \{0, 1\}^* \rightarrow \mathbb{N}$  be the inverse mapping of  $\text{bi}$ .

Then

$$L \in \text{Space}_{\{0,1\}}(n) \Rightarrow \text{un}(L) \in \text{Space}_{\{0\}}(\log_2 n)$$

and

$$L \in \text{Space}_{\{0\}}(\log_2 n, m) \Rightarrow \text{bi}(L - \{\varepsilon\}) \in \text{Space}_{\{0,1\}}(n, 2m+1).$$

The first of these results is obvious. In order to prove the second result we define a Turing machine whose working tape is divided into two tracks. The lower one stores during the simulation the same inscription as the Turing machine  $M$  which accepts  $L$ . The upper track stores the position of the input head of  $M$  in binary notation. We need one additional symbol in order to encode the position of the work tape head.

From the above results we get immediately the following:

$$L \in \text{Space}_{\{0\}}(n) \Rightarrow \text{un}(L) \in \widetilde{\text{SPACE}}(\log n)$$

and

$$L \in \widetilde{\text{SPACE}}_{\{0\}}(\log_2 n, m) \Rightarrow \text{bi}(L - \{\varepsilon\}) \in \text{Space}_{\{0\}}(n, 2m+1).$$

Now we are ready to prove the lemma. Suppose there exists some  $k \in \mathbb{N}$  such that  $\tilde{H}(k) = \widetilde{\text{SPACE}}(\log n)$ .

Then the following implication holds:

$$\begin{aligned} L \in \text{Space}_{\{0\}}(n) & \Rightarrow \text{un}(L) \in \widetilde{\text{SPACE}}(\log n) = \tilde{H}(k) \\ & \Rightarrow \text{un}(L) \in \widetilde{\text{SPACE}}(\log_2 n, 2^k). \\ & \Rightarrow L = \text{bi}(\text{un}(L)) \in \text{Space}_{\{0\}}(n, 2^{k+2}). \end{aligned}$$

Therefore  $\widetilde{\text{SPACE}}(\log n) = \tilde{H}(k)$  implies

$$\text{Space}_{\{0\}}(n) = \text{Space}_{\{0\}}(n, 2^{k+2})$$

which is a contradiction to the result mentioned at the beginning of this proof.  $\square$

**Proof of lemma 2**

Let  $L \in \text{Space}(\log n)$  be an arbitrary chosen language and let  $M$  be a Turing machine accepting  $L$  within space bound  $\log n$ .

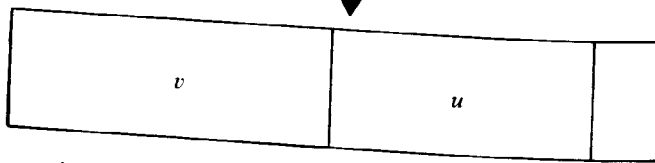
Let  $M'$  be the following modification of  $M$ :

1.  $M'$  writes  $\text{bi}(n)$ , where  $n$  is the input number, on its working tape ( $\text{bi}$  is defined as in the proof of lemma 1).

2. During the rest of the computation  $M'$  never uses its input tape again.  $M'$  simulates  $M$  and during this simulation its working tape is divided into 3 tracks. On its first track  $M'$  stores  $\text{bi}(n)$ , on its second track the position of the input head of  $M$  in binary notation and on its third track the inscription of the worktape of  $M$ .

Furthermore we can define  $M'$  in such a way that it has only two worktape symbols. There exists a  $k \in \mathbb{N}$  such that  $M'$  uses for every computation at most  $k \cdot \log_2 n$  cells on its worktape.

We will now define a 3-register machine  $A$  accepting  $T_k(L)$ . We apply a method which is used quite often in order to simulate Turing machines by 3-register machines. The working tape of  $M'$  is divided by the head position into two parts



and  $A$  stores during the simulation on its first two counters the numbers  $un(u)$  and  $un(v^R)$ . In order to simulate one step of  $M'$  the register machine  $A$  has to divide or multiply these registers by two and to add  $+1$  or  $-1$ . This can be done by using the third register.

In order to initiate this simulation  $A$  tests whether the input number is of the form  $2^{k \cdot m}$ ,  $m \in \mathbb{N}$  and sets its second counter to  $un(\text{bi}(2^m)^R) = un(00 \dots 0) = 2^m$ .

These computations can be performed by a 3-register machine.

Since  $M'$  uses at most  $k \cdot \log_2 n = k \cdot m$  cells the numbers stored by the registers are bounded by  $2^{k \cdot m}$  during the whole computation.

It is clear that  $A$  accepts some number  $2^{k \cdot m}$  if and only if  $M'$  accepts  $2^m$ .  $\square$

**Proof of lemma 3**

The result is true for  $k=1$ . Now suppose that  $k \geq 2$ .

Let  $M$  be a  $j$ -head automaton accepting  $T_k(L)$ . Our  $k \cdot j$ -head automaton tests first whether the input is of the form  $O^{2^n}$ . It needs two heads to do this. Now suppose  $\tilde{M}$  reads an input string  $O^{2^n}$ . Then it starts to simulate  $M$ . During this simulation it encodes each head position  $h$ ,

$$0 \leq h < 2^{k \cdot n}, \quad h = l_1 + l_2 \cdot 2^n + \dots + l_k \cdot 2^{(k-1) \cdot n}, \quad 0 \leq l_v < 2^n,$$

of  $M$  by the positions  $l_1, \dots, l_k$  of  $k$  of its heads.

Note that  $h=0$  iff  $l_v=0$  for all  $v=1, \dots, k$  and that  $h=2^{k \cdot n}+1$  iff  $M$  moves starting from  $h=2^{k \cdot n}-1$  ( $l_v=2^n-1, \forall v=1, \dots, k$ ) two cells to the right. It is clear that  $\tilde{M}$  can simulate the moves of the heads of  $M$  since it can test easily whether  $l_v=0$  or  $l_v=2^n-1$ .  $\square$

**Proof of lemma 4**

Let  $M$  be a  $j$ -head automaton accepting  $T_{k+1}(L)$ .

We have to construct a  $(j+1)$ -head automaton  $\tilde{M}$  accepting  $T_k(L)$ .

It can be tested easily (using 2 heads) whether the input is of the form  $O^{2^{k \cdot n}}$  for some  $n \in \mathbb{N}$ . If this is the case then  $\tilde{M}$  has to test whether  $O^{2^{(k+1) \cdot n}}$  is accepted by  $M$ . In order to do so  $\tilde{M}$  encodes each head position  $h_v$ ,  $1 \leq v \leq j$ , of  $M$ :

$$0 \leq h_v \leq 2^{(k+1) \cdot n} + 1$$

by the position of its own  $v$ -th head  $\tilde{h}_v$ :

$$0 \leq \tilde{h}_v \leq 2^{k \cdot n} + 1$$

and by an additional number  $\sigma_v$ ,

$$0 \leq \sigma_v < 2^n$$

in such a form that always  $h_v = \tilde{h}_v + \sigma_v \cdot 2^{k \cdot n}$ .

Note that  $h_v = 2^{(k+1) \cdot n} + 1$  if and only if  $\tilde{h}_v = 2^{k \cdot n} + 1$  and  $\sigma_v = 2^n - 1$ .

$\tilde{M}$  uses its  $(j+1)$ -st head to store the  $j$  numbers  $\sigma_1, \dots, \sigma_j$  in the form

$$\tilde{h}_{j+1} = \sigma_1 + \sigma_2 \cdot 2^n + \dots + \sigma_j \cdot 2^{(j-1) \cdot n} + 2^{(k-1) \cdot n}.$$

This is possible since  $j < k$ .

First  $\tilde{M}$  has to encode the initial head positions of  $M$ . That means it has to set  $\tilde{h}_{j+1} \leftarrow 2^{(k-1) \cdot n}$ . This can be done easily (using 3 heads).

During the simulation  $\tilde{M}$  always stores in its finite memory which of the  $\sigma_v$ , encoded by  $\tilde{h}_{j+1}$ , are equal to  $2^n - 1$ .  $\tilde{M}$  has to simulate the moves  $\eta_v$ ,  $\eta_v \in \{-1, 0, +1\}$ , of the  $j$  heads of  $M$ . Furthermore it has to decide which of the new  $\sigma_v$  are equal to  $2^n - 1$ . This is simple if  $0 \leq \tilde{h}_v + \eta_v \leq 2^{k \cdot n} + 1$ . In this case  $\tilde{M}$  only has to set  $\tilde{h}_v \leftarrow \tilde{h}_v + \eta_v$ .  $\sigma_v$  remains unchanged.

Now suppose  $\tilde{h}_v = 2^{k,n} + 1$  and  $\eta_v = +1$ . (The case  $\tilde{h}_v = 0$  and  $\eta_v = -1$  leads to analogous considerations.) Then  $\tilde{M}$  has to set  $\tilde{h}_v \leftarrow 2$ ,  $\sigma_v \leftarrow \sigma_v + 1$ . (Note that  $\tilde{h}_v = 2^{k,n} + 1$  and  $\eta_v = +1$  imply  $\sigma_v < 2^n - 1$ , since  $\tilde{h}_v + \sigma_v \cdot 2^{k,n} \leq 2^{(k+1),n} + 1$ .)

Performing the operation on  $\sigma_v$  is the difficulty in this proof. The  $\sigma_1, \dots, \sigma_j$  will be stored always by the position of the  $(j+1)$ -st head but we shall rotate their sequence and we shall be able to add +1 when  $\sigma_v$  is carried from the last position to the first position.

In order to do this we need the  $(j+1)$ -st head, the  $v$ -th head and one further head (we can assume that  $v \neq 1$  and in this case we take the first head). Note that the  $v$ -th head does not store anything and therefore it is free for intermediate computations.

We will denote the position of the first head by  $\kappa$  and the position of the  $(j+1)$ -st head by  $\lambda$ . In the case  $j < k-1$  it is favourable to introduce the new numbers  $\sigma_{j+1} = \sigma_{j+2} = \dots = \sigma_{k-1} = 0$ .

$$\lambda = \sum_{\mu=1}^{k-1} \sigma_{\mu} \cdot 2^{(\mu-1),n} + 2^{(k-1),n}.$$

Furthermore we can assume that  $\kappa < 2^{k,n}$  ( $\tilde{M}$  can test whether  $\kappa < 2^{k,n}$  by going two cells to the right. If  $\kappa \geq 2^{k,n}$  then we set  $\kappa = 2^{k,n} - 1$  and store the difference in the finite memory.) We decompose  $\kappa$  in the form

$$\kappa = \psi_1 + \psi_2 \cdot 2^n \quad \text{with} \quad 0 \leq \psi_1 < 2^n, \quad 0 \leq \psi_2 < 2^{(k-1),n}.$$

Now we are ready to describe the rotation technique.

1.  $\tilde{M}$  changes the positions of head 1 and head  $(j+1)$  into

$$\kappa = \psi_2 + 2^{(k-1),n},$$

$$\lambda = R_n(\psi_1) + \sigma_1 \cdot 2^n + \dots + \sigma_{k-1} \cdot 2^{(k-1),n},$$

where for any  $x < 2^n$   $R_n(x)$  is defined in the following way:

Let  $\varphi_n(x) \in \{0, 1\}^*$ ,  $|\varphi_n(x)| = n$ , be the binary notation of  $x$  lengthened by an appropriate number of leading zeros. Then  $R_n(x) < 2^n$  is that number whose binary notation of length  $n$  (again allowing leading zeros) is the reversal of  $\varphi_n(x)$ . Note that  $R_n(R_n(x)) = x$  for all  $x < 2^n$ .

$\tilde{M}$  reaches the above head positions by the application of the following algorithm:

$$\kappa \leftarrow \kappa + 2^{k,n}$$

While  $\lambda < 2^{k,n}$  Do

$$\begin{aligned} \text{Begin } \kappa &\leftarrow \left\lfloor \frac{\kappa}{2} \right\rfloor \text{ and } \alpha \leftarrow \kappa - 2 \left\lfloor \frac{\kappa}{2} \right\rfloor \\ \lambda &\leftarrow \alpha + 2 \lambda \end{aligned}$$

End

$$\lambda \leftarrow \lambda - 2^{k,n}$$

It is clear that  $\tilde{M}$  can perform this computation using its  $v$ -th head during the realization of the division. In order to see that this algorithm is correct set  $\varphi_n(\psi_1) = a_{n-1} \dots a_0 \in \{0, 1\}^*$ . If we further denote by  $\varphi : \mathbb{N} \rightarrow \{0\} \cup \{1\} \circ \{0, 1\}^*$  the binary decomposition, then after  $i$  loops,  $0 \leq i \leq n$ , the following holds:

$$\varphi(\kappa) = 1 \ 0 \dots 0 \ \varphi(\psi_2) \ a_{n-1} \dots a_i,$$

$$\langle (k-1) \cdot n \rangle$$

$$\varphi(\lambda) = 1 \ \varphi_n(\sigma_{k-1}) \dots \varphi_n(\sigma_1) \ a_0 \dots a_{i-1}.$$

Therefore  $|\varphi(\lambda)| = (k-1) \cdot n + i$  and this implies  $\lambda < 2^{k,n}$  for  $i < n$  and  $\lambda \geq 2^{k,n}$  for  $i = n$ . The loop is carried out exactly  $n$  times and this leads to the head positions  $\kappa$  and  $\lambda$  which we wanted:

2.  $\tilde{M}$  changes the position of head 1 and head  $(j+1)$  into

$$\kappa = \sigma_{k-1} + \psi_2 \cdot 2^n,$$

$$\lambda = R_n(\psi_1) + 2^n \cdot \sigma_1 + \dots + 2^{(k-2),n} \cdot \sigma_{k-2} + 2^{(k-1),n}.$$

First  $\tilde{M}$  changes  $\lambda$  into

$$\lambda = \overline{R_n(\psi_1)} + 2^n \cdot \sigma_1 + \dots + 2^{(k-1),n} \cdot \sigma_{k-1},$$

where

$$\overline{R_n(\psi_1)} = \begin{cases} R_n(\psi_1), & \text{if } R_n(\psi_1) \equiv 1 \pmod{2}, \\ R_n(\psi_1) + 1, & \text{otherwise.} \end{cases}$$

Note that only the lowest bit of  $\lambda$  is changed in such a way that  $\lambda$  becomes an odd number. (It is stored in the finite memory whether  $R_n(\psi_1)$  is odd or even.)

Afterwards  $\tilde{M}$  performs the following algorithm:

While  $\kappa < 2^{k,n}$  Do

Begin  $\alpha \leftarrow 2^{k,n}$ -th bit of  $\lambda$

$$\text{If } \alpha = 1 \text{ then } \lambda \leftarrow 2 \lambda - 2^{k,n}$$

$$\text{If } \alpha = 0 \text{ then } \lambda \leftarrow 2 \lambda$$

$$\kappa \leftarrow \kappa + 2 \alpha$$

End

In order to see what is done by this algorithm we consider the decomposition

$$\lambda = \tilde{\lambda} + \alpha 2^{k,n-1}, \quad \tilde{\lambda} < 2^{k,n-1}.$$

Then  $\alpha = 1$  iff  $2 \lambda \geq 2^{k,n}$ . Furthermore the second and third statement in the block generate  $\lambda \leftarrow 2 \tilde{\lambda}$ . As in 1 it can be seen that the while loop is carried out exactly  $n$  times. During this algorithm the number  $\sigma_{k-1}$  is carried over from  $\lambda$  to  $\kappa$  bit by bit. Therefore  $\kappa$  and  $\lambda$  are changed by this algorithm into

$$\begin{aligned} \kappa &= \sigma_{k-1} + 2^n \psi_2 + 2^{k,n}, \\ \lambda &= 2^n \widetilde{R_n(\psi_1)} + 2^n \sigma_1 + \dots + 2^{(k-1),n} \sigma_{k-2}. \end{aligned}$$

We obtain the head positions which we wanted by:

- (a) subtracting  $2^{k,n}$  from  $\kappa$ ;
  - (b) adding  $2^{k,n}$  to  $\lambda$ ;
  - (c) dividing  $\lambda$  by 2 as long as the remainder is 0;
  - (d) changing  $\widetilde{R_n(\psi_1)}$  into  $R_n(\psi_1)$ .
- (Note that  $2^{k,n}$  is given by the position of the right endmarker.)

In the following let us use a simple abbreviation.  
Instead of

$$\begin{aligned} \kappa &= \alpha_0 + \psi_2 2^n, & \lambda &= \sum_{\mu=1}^{k-1} \alpha_\mu 2^{(\mu-1),n} + 2^{(k-1),n}, \\ 0 &\leq \alpha_\mu < 2^n, & \forall \mu &= 0, \dots, k-1, \end{aligned}$$

we write

$$(\kappa; \lambda) = (\alpha_0; \alpha_1, \dots, \alpha_{k-1}).$$

The application of the algorithm described in 1 and 2 induces the transition

$$(\alpha_0; \alpha_1, \dots, \alpha_{k-1}) \rightarrow (\alpha_{k-1}; R_n(\alpha_0), \alpha_1, \dots, \alpha_{k-2}).$$

Therefore we get by  $k-v$  applications

$$\begin{aligned} (\psi_1; \sigma_1, \dots, \sigma_{k-1}) &\rightarrow (\sigma_{k-1}; R_n(\psi_1), \sigma_1, \dots, \sigma_{k-2}) \rightarrow \dots \\ &\rightarrow (\sigma_v; R_n(\sigma_{v+1}), \dots, R_n(\sigma_{k-1}), R_n(\psi_1), \sigma_1, \dots, \sigma_{v-1}). \end{aligned}$$

Now 1 is applied again. Since during this computation  $\sigma_v$  is carried over from  $\kappa$  to  $\lambda$  bit by bit  $\tilde{M}$  is able to add +1 (performing the binary addition of +1) and to

test whether the new  $\sigma_v$  is equal to  $2^n - 1$  (this is true iff all bits which are carried over are equal to one). Afterwards we apply 2 and get the head positions

$$(\sigma_{v-1}; R_n(\sigma_v + 1), R_n(\sigma_{v+1}), \dots, R_n(\sigma_{k-1}), R_n(\psi_1), \sigma_1, \dots, \sigma_{v-2}).$$

Since  $R_n(R_n(\alpha)) = \alpha$  for all  $\alpha < 2^n$  a further application of this rotation technique leads to

$$(\psi_1; \sigma_1, \dots, \sigma_{v-1}, \sigma_v + 1, \sigma_{v+1}, \dots, \sigma_{k-1}).$$

By this whole computation we get the old position of the first head again, we have changed the position of the  $(j+1)$ st head in such a way that  $\sigma_v$  is replaced by  $\sigma_v + 1$  and we have tested whether the new  $\sigma_v$  is equal to  $2^n - 1$ . This shows that  $\tilde{M}$  is able to simulate  $M$  step by step.  $\square$

#### 4. HIERARCHY RESULTS FOR COUNTER AUTOMATA AND REGISTER MACHINES

Theorem 1 leads immediately to hierarchy results for the complexity classes defined by counter automata and register machines. Since

$$R(k) \subset C_{\{0\}}(k-1) \subset H_{\{0\}}(k) \not\subseteq H_{\{0\}}(k+1) \subset C_{\{0\}}(k+1) \subset R(k+3),$$

we get immediately

$$C_{\{0\}}(k) \not\subseteq C_{\{0\}}(k+2), \quad \forall k \geq 0$$

and

$$R(k) \not\subseteq R(k+3), \quad \forall k \geq 1,$$

and the same results hold in the nondeterministic case.

In the following we will improve three of these four results. Let us consider first register machines. We show that for languages containing only elements of the form  $2^m$ ,  $m \in \mathbb{N}$ , only  $k+1$  registers are necessary in order to simulate a  $k$ -head automaton.

LEMMA 5 :

$$\tilde{H}(k) \subset \tilde{R}(k+1), \quad \widetilde{NH(k)} \subset \widetilde{NR(k+1)} \quad \text{for } k \geq 2.$$

*Proof:* The proof is the same for deterministic and for nondeterministic automata. We consider here the deterministic case. Let  $L \subset \{O^{2^n}; n \in \mathbb{N}\}$  be some language such that there exists a  $k$ -head automaton  $M$  accepting  $L$ . We will define a  $(k+1)$ -register machine  $\tilde{M}$ .

$\tilde{M}$  tests first whether its input number is of the form  $2^n$  by dividing the input number successively by two and storing the number of divisions by its third

register. If the input number is of the form  $2^n$  then afterwards  $n$  is stored by register 3.  $M$  checks whether  $n$  is an even or an odd number (we will see later why this is done) and computes  $2^n$  again afterwards.

Now suppose the input number is  $2^n$ . Then  $\tilde{M}$  simulates  $M$ . In order to do so it stores the head positions  $h_1, \dots, h_k$  of  $M$  by its first  $k$  registers in the form

$$r_v = \tilde{h}_v + 2^n, \quad \forall v = 1, \dots, k,$$

where

$$\tilde{h}_v = \begin{cases} h_v, & \text{if } 0 \leq h_v \leq 2^n, \\ 2^n, & \text{if } h_v = 2^n + 1. \end{cases}$$

(If  $h_v = 2^n + 1$  then additionally a corresponding bit is stored in the finite memory of  $\tilde{M}$ .) Note that  $0 \leq r_v \leq 2 \cdot 2^n$  holds for all  $v = 1, \dots, k$ .

First  $M$  has to initiate this encoding by setting  $r_1 = 2^{n+1}$  and  $r_2 = \dots = r_k = 2^n$ . In order to simulate one step of  $M$  the register machine  $\tilde{M}$  has to decide for all  $v = 1, \dots, k$  whether  $\tilde{h}_v = 0$  or  $\tilde{h}_v = 2^n$ . Note that this is the case if and only if  $r_v$  is a power of two. Therefore  $\tilde{M}$  proceeds in the following way:

(a) It checks whether there exists  $\mu \in \{1, \dots, k\}$  such that  $r_\mu \neq r_v$ . (This can be done by means of register  $k+1$ .)

(b) Let such a  $\mu$  exist and suppose that  $r_\mu < r_v$ . Then  $\tilde{M}$  divides the  $v$ -th register successively by two (using register  $k+1$ ) as long as the remainder is zero. The number of divisions is not stored. In this way  $\tilde{M}$  decides whether  $r_v$  is a power of two. If this is the case then  $r_v = 2^{n+1}$ , since  $2^n \leq r_v$ ,  $r_\mu \leq 2^{n+1}$  and  $r_\mu < r_v$ . Afterwards  $\tilde{M}$  multiplies the  $v$ -th register by two as long as the content of register  $v$  is smaller than  $r_\mu$  (content of register  $\mu$ ). It has computed  $r_v$  again when register  $v$  stores for the first time a number greater than  $r_\mu$ . (It is clear that in the same way  $\tilde{M}$  decides whether  $r_v = 2^n$  if  $r_\mu > r_v$ .)

(c)  $r_1 = r_\mu$ ,  $\forall \mu = 2, \dots, k$ .

In this case  $\tilde{M}$  uses 3 of its register to divide  $r_1$  successively by two (as long as the remainder is zero) and to store the number of divisions in its register 3. (Therefore it can recompute  $r_1$  again.) If  $r_1$  is a power of two, then  $r_1 = 2^n$  or  $r_1 = 2^{n+1}$ .  $\tilde{M}$  checks whether the number stored by register 3 (this is  $n$  or  $n+1$ ) is an even or an odd number. Knowing whether  $n$  is an even or an odd number  $\tilde{M}$  can decide whether  $r_1 = 2^n$  or  $r_1 = 2^{n+1}$ .  $\square$

From lemma 5 and theorem 1 we get immediately:

THEOREM 2: For all  $j \in \mathbb{N}$ :

$$R(j) \not\subseteq R(j+2) \quad \text{and} \quad NR(j) \not\subseteq NR(j+2).$$

Let us consider now deterministic counter automata. We will prove a result analogous to lemma 4.

LEMMA 6: For all  $L \in \text{Space}(\log n)$  and for  $k > j \geq 2$ :

$$T_{k+1}(L) \in \tilde{C}(j-1) \Rightarrow T_k(L) \in \tilde{C}(j).$$

*Proof:* The proof is very much the same as the proof of lemma 4. Let  $M$  be a deterministic  $(j-1)$  counter automaton accepting  $T_{k+1}(L)$  whose counters are always bounded by the input number. We have to construct a deterministic  $j$ -counter automaton  $\tilde{M}$  accepting  $T_k(L)$ . There will exist a constant  $d \in \mathbb{N}$  such that its counters are bounded by  $dm$  if  $m$  is the input number.

Suppose the input is of the form  $O^{2^{k,n}}$  with some  $n \in \mathbb{N}$  (this can be checked easily by  $M$ ). In this case  $\tilde{M}$  simulates  $M$ . It encodes the head position  $h$  and the contents  $c_v$ ,  $1 \leq v \leq j-1$ , of the counters of  $M$  by its own head position  $\tilde{h}$  and the contents  $\tilde{c}_v$ ,  $1 \leq v \leq j$ , of its own counters in such a way that

$$0 \leq \tilde{h}, \tilde{c}_j \leq 2^{k,n},$$

$$0 \leq \tilde{c}_v \leq d \cdot 2^{k,n}, \quad \forall v = 1, \dots, j-1$$

with some constant  $d \in \mathbb{N}$  (which will be determined later) and

$$h = \tilde{h} + \sigma_0 \cdot 2^{k,n},$$

$$c_v = \tilde{c}_v + \sigma_v \cdot 2^{k,n}, \quad \forall v = 1, \dots, j-1,$$

with some  $\sigma_v$ ,

$$0 \leq \sigma_v < 2^n, \quad \forall v = 0, \dots, j-1$$

and

$$\tilde{c}_j = \sigma_0 + \sigma_1 \cdot 2^n + \dots + \sigma_{j-1} \cdot 2^{(j-1) \cdot n} + 2^{(k-1) \cdot n}$$

hold.

This encoding already shows that this proof is slightly more difficult than the proof of lemma 4. We allow the  $\tilde{c}_v$  to grow up to  $d$  times the length of the input. This is necessary because (in contrast to the situation of lemma 4) we can't test in each step whether the number stored by a counter is equal to  $2^{k,n}$ . This is possible only if one of the counters is set to zero (or if the head reaches an endmarker). Therefore we must allow the counters to grow until one of them (or the head) is free. We show in the following that during such a computation the counters grow at most up to  $d \cdot 2^{k,n}$  for some  $d$ .

During the simulation of one step of  $M$  the automaton  $M$  has to distinguish two cases:

(a) The head of  $\tilde{M}$  does not scan one of the endmarkers and  $\tilde{c}_v > 0$  for all  $v=1, \dots, j-1$ . In this case also the head of  $M$  does not scan one of the endmarkers and also  $c_v > 0$  for all  $v=1 \dots j-1$ .  $\tilde{M}$  simply changes its head position and its counters in the same way as  $M$  would change its head position and its counters.

(b)  $\tilde{h}=0$  or  $\tilde{h}=2^{k,n}+1$  or  $\tilde{c}_v=0$  for some  $v \in \{1, \dots, j-1\}$ . In this situation  $\tilde{M}$  has one counter free for intermediate computations. It checks for all  $v=1, \dots, j$  whether  $\tilde{c}_v \geq 2^{k,n}$  and if this is the case it performs  $\tilde{c}_v \leftarrow \tilde{c}_v - 2^{k,n}$  and  $\sigma_v \leftarrow \sigma_v + 1$  as long as  $\tilde{c}_v \geq 2^{k,n}$  holds. In order to change  $\sigma_v$  it uses again the rotation technique described in detail in the proof of lemma 4. Note that  $M$  has one counter free and therefore it can compare the content of one of its counters with  $2^{k,n}$  whenever it wants it. It is clear that  $M$  can change the  $\tilde{h}$ ,  $\tilde{c}_v$  and  $\sigma_v$ ,  $0 \leq v \leq j-1$ , according to the changes performed by  $M$ .

In this way  $\tilde{M}$  simulates each step of  $M$  and we can construct  $\tilde{M}$  in such a way that it accepts  $T_k(L)$ . It remains to show that there exists a constant  $d \in \mathbb{N}$  such that  $\tilde{c}_v \leq d \cdot 2^{k,n}$  holds during the whole simulation for all  $v=1, \dots, j-1$ .

Suppose (b) holds, that means the head of  $M$  scans one of its endmarkers or one of the counters is zero and suppose that  $\tilde{c}_v \leq 2^{k,n}$  for all  $v=1, \dots, j-1$ . (This is true when  $M$  starts its computation.) Let us estimate the number  $r$  of moves which are performed by  $\tilde{M}$  before  $\tilde{M}$  reaches again a configuration of the type (b). During this computation the moves of  $M$  are determined uniquely by its states. Suppose  $M$  has  $p$  states and  $s$  is the state in the beginning of the computation we investigate here. Then  $r \leq p$  or there exist numbers  $q_0, q_1 \leq p$  and a state  $t$  such  $M$  reaches the states

$$s \xrightarrow{q_0 \text{ moves}} \dots \rightarrow t \xrightarrow{q_1 \text{ moves}} \dots \rightarrow t.$$

Let  $\delta_v^0, \delta_v^1, 0 \leq v \leq j-1$ , be the distances by which the head position and the first  $j-1$  counters of  $M$  are changed during the first  $q_0$  moves and during the following  $q_1$  moves, respectively. During this computation  $\tilde{M}$  moves its head and its first  $j-1$  counters in the same way as  $M$  does and therefore (since  $M$  halts)  $\delta_0^1 \neq 0$  or there exists  $v_0 \in \{1, \dots, j-1\}$  such that  $\delta_{v_0}^1 < 0$ .

Set  $\mu=0$  or  $\mu=v_0$ , respectively.

[When we want to accentuate the dependence of  $\mu, \delta_v^0, \delta_v^1$  on  $s$  we will write  $\mu(s), \delta_v^0(s), \delta_v^1(s)$ .]

Therefore  $\tilde{M}$  reaches again a configuration of the type (b) after at most

$$r = \frac{2^{k,n} + \delta_\mu^0}{\delta_\mu^1} \cdot q_1 \text{ moves.}$$

The contents of the counters  $v \in \{1, \dots, j-1\} - \{\mu\}$  are bounded by  $2^{k,n} + \delta_v^0 + \delta_v^1 r$ . And for sufficiently large  $n$   $2^{k,n} + \delta_v^0 + \delta_v^1 r \leq d \cdot 2^{k,n}$  for some  $d \in \mathbb{N}$ .

Whenever  $\tilde{M}$  reaches a configuration of the type (b) it changes the numbers stored by its first  $j-1$  counters into numbers

$$\tilde{c}_v \leq 2^{k,n} \quad \text{for all } v=1, \dots, j-1,$$

and therefore because of the above considerations

$$\tilde{c}_v \leq d \cdot 2^{k,n} \quad \text{holds for all } v=1, \dots, j-1$$

during the whole computation of  $\tilde{M}$ .

Furthermore the counters of  $M$  are always bounded by  $2^{(k+1) \cdot n}$ . This implies that  $\sigma_v < 2^n$  holds for all  $v=1, \dots, j-1$  during the whole computation of  $\tilde{M}$ .  $\square$

THEOREM 3: For all  $j \in \mathbb{N}$ :

$$C_{\{0\}}(j) \not\subseteq C_{\{0\}}(j+1) \quad \text{and} \quad NC_{\{0\}}(j) \not\subseteq NC_{\{0\}}(j+2).$$

*Proof:* The nondeterministic case was already proved in the beginning of this section. The deterministic case follows from lemma 6 in the same way as theorem 1 follows from lemma 4.  $\square$

It should be noted that the proof of lemma 6 really uses the determinism of the automata. The argument holds also in the nondeterministic case if one is able to show that for any input string there exists a computation where the number of moves performed without reaching an endmarker is bounded in the same way as in the proof of lemma 6.

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